

**APPLICATION TRANSFER REQUEST FOR S.N.** \_\_\_\_\_

**Section I. TRANSFER REQUEST BY (PRINT NAME)** Chaudry

Date 08/20/02

TO: Art Unit 2122

Class/sub 717/100+

FROM: A.U. 2133

Class 714

**REASON:**

Scripting approach for managing test bench. Partitioning the functionality of a test bench between Verilog and scripting language allows for a significant reduction in compile time. No actual test. Only development of a process.

Hand carried: Personally accepted by \_\_\_\_\_

**Section II a. DISPOSITION BY RECEIVING TC**

By: \_\_\_\_\_

A.U. \_\_\_\_\_

Date \_\_\_\_\_

☐ ACCEPTED BY RECEIVING T.C.

NOT ACCEPTED

☐ Forward to receiving TC Post Classifier

☐ Non-classification issue/other, return to Originating TC/AU \_\_\_\_\_

**REASON:**

**Section II b. DISPOSITION BY RECEIVING TC POST CLASSIFIER**

☐ This dispute was resolved. Forward to TC/AU \_\_\_\_\_ Class/Sub \_\_\_\_\_ Post Classifier \_\_\_\_\_ Date \_\_\_\_\_

Concurring \_\_\_\_\_ Date \_\_\_\_\_

☐ This dispute was not resolved, forward to DISPUTE RESOLUTION PANEL

**Post Classifier Assessment:**

Post Classifier \_\_\_\_\_ Date \_\_\_\_\_

**Section III. DISPOSITION BY DISPUTE RESOLUTION PANEL**

Date \_\_\_\_\_

**Panel Decision:** Forward to Technology Center / Art Unit \_\_\_\_\_ Class/sub \_\_\_\_\_

**REASON:**

Panel Member \_\_\_\_\_ Concurring Panel Member \_\_\_\_\_

☐ This application MAY NOT be returned to the dispute resolution panel. THIS IS A FINAL DISPOSITION.